



12-bit 100KSPS single-channel Analog-to-Digital Converter (ADC)

1 Main features:

- ◆ Conversion bits: 12 bits
- ◆ Throughput rate: 100 KSPS
- ◆ Power consumption: 40mW
- ◆ INL : ±1LSB(Typical value)
- ◆ SNR : 70dB@10kHzinput
- ◆ Input range : ±5V、 ±10V、 0~10V、 0~20V
- ◆ Compatible with 8b or 16b parallel interfaces
- ◆ Encapsulation : SOP28/DIP28

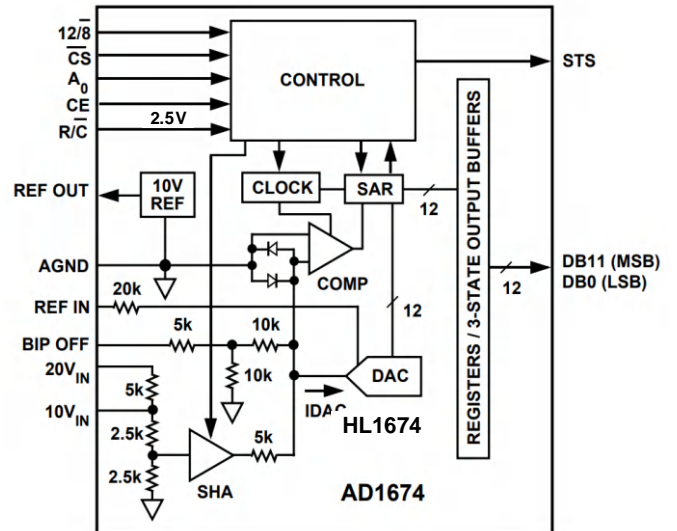
2. Typical applications

- ◆ Test equipment
- ◆ analyzer

3 Product Description

This chip is a 12-bit, 100KSPS successive approximation type Analog-to-digital converter (ADC), after factory calibration can achieve 1LSB linearity,

this chip is compatible with foreign products AD1674 pin, can be replaced, the functional structure of the chip block diagram as shown below:



4 Product Highlights

- ◆ On-chip integrated reference voltage
- ◆ Flexible configuration of input signal range
- ◆ Compatible with 8b and 16b parallel interfaces

5 Compared with similar foreign products

	precision	Conversion rate	Data port	Power dissipation	SNDR	THD	Encapsulation form
AD1674 (ADI)	12-bit	100KSPS	Parallel interface	200mW	70dB@10 kHz	-90dB@10kHz	DIP28
HL1674	12-bit	100KSPS	Parallel interface	40mW	70dB@10 kHz	-90dB@10kHz	DIP28