14-bit 100KSPS single-channel Analog-to-Digital Converter (ADC)

1 Main features:

- Converted bits: 14 bits
- Throughput rate: 100 KSPS
- Low power consumption: 4~17mW
- INL：±0.5LSB(Typical value)
- SNDR：81dB@10kHz input
- THD：-98dB@10kHz input
- Pseudo differential input range：0～V_{DD}（V_{DD}=2.5~5.5）
- Pipeline-free delay
- Serial interface: SPI compatible
- Encapsulation：SOT23

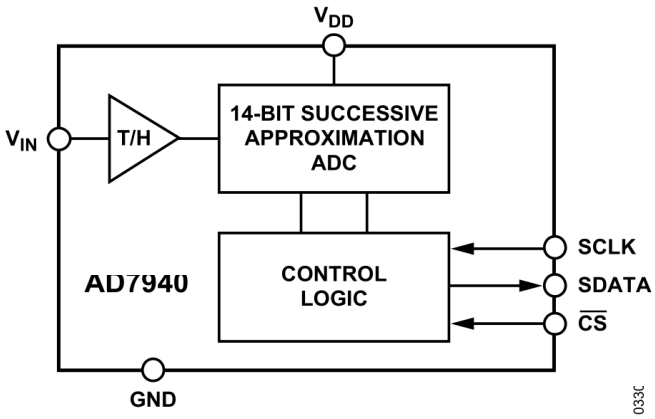
2. Typical applications

- Battery powered equipment
- communication
- Automatic test equipment
- Data acquisition
- Medical instrument

3 Product Description

This chip is a 14-bit, successive approximation analog-to-digital converter (ADC), which uses a single power supply, and the power supply voltage is used as the reference signal of the ADC, which greatly simplifies the peripheral circuit of the chip.

It has a low-power, high-precision 14-bit sampling ADC and a serial interface port. At the falling edge of the CS, the device samples the analog input voltage of the VIN port, ranging from 0 to VDD. The chip is compatible with the foreign product AD7940 pin and can be replaced. The functional structure block diagram of the chip is shown as follows:



4 Product Highlights

- Minimalist package design
- The power supply ranges from 2.5 to 5.5V
- Ultra-low power standby mode

5 Compared with similar foreign products

	precision	Conversion rate	Data port	Power dissipation	SNDR	THD	Encapsulation form
AD7940（ADI）	14-bit	100KSPS	serial	4~17mW	81dB@10 kHz	-98dB@10kHz	SOT23
HL7940	14-bit	100KSPS	serial	4~17mW	81dB@10 kHz	-98dB@10kHz	SOT23

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