



## 14-bit 1MSPS dual dual-channel Analog-to-Digital Converter (ADC)

### 1 Main features:

- ◆ Converted bits: 14 bits
- ◆ Throughput rate: 1 MSPS
- ◆ Low power consumption: 85mW
- ◆ INL:  $\pm 3\text{LSB}$  (Typical value)
- ◆ SNDR: 75dB@50kHz input
- ◆ Input range:  $\pm 5\text{V}$ ,  $\pm 10\text{V}$ ,  $0\sim 10\text{V}$  ( $V_{\text{REF}}=2.5\text{V}$ )
- ◆ Pipeline-free delay
- ◆ Serial interface: SPI compatible
- ◆ Encapsulation: CSOP24/TSSOP24

### 2. Typical applications

- ◆ Power supply equipment
- ◆ Servo control system
- ◆ Automatic test equipment
- ◆ Data acquisition
- ◆ Medical instrument

### 3 Product Description

The HL7367/HL7366 is a dual-channel, dual-channel, 14/12-bit, 1MSPS rate low-power sequential approximation analog-to-digital converter (ADC).

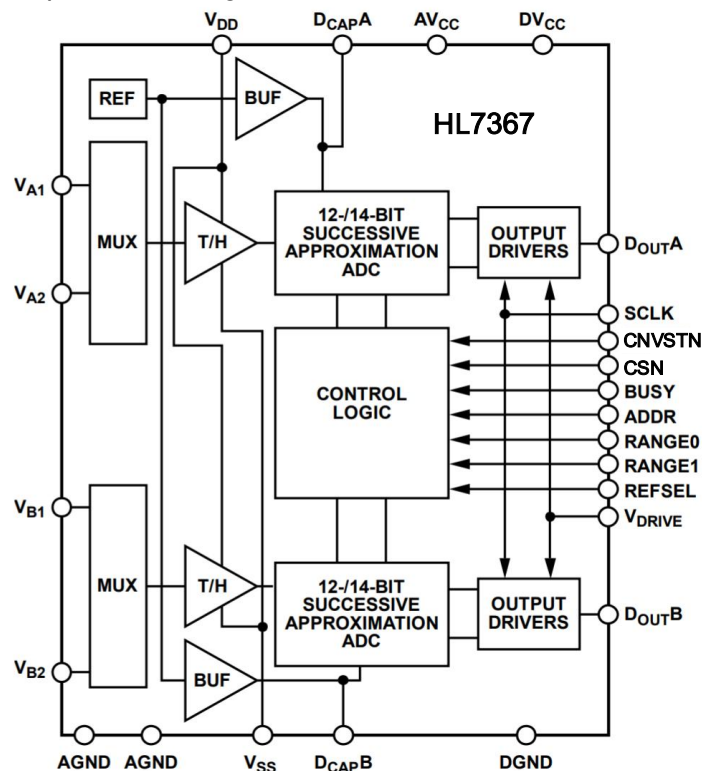
The HL7367/HL7366 quantizes two inputs simultaneously and is compatible with the high-speed SPI interface.

The HL7367/HL7366 can be configured to quantify input signals in the range of  $\pm 5\text{V}$ ,  $\pm 10\text{V}$ , and  $0\sim 10\text{V}$ .

### 5 Compared with similar foreign products

|              | precision | Conversion rate | Data port | Power dissipation | SNDR        | THD         | Encapsulation form |
|--------------|-----------|-----------------|-----------|-------------------|-------------|-------------|--------------------|
| AD7367 (ADI) | 14-bit    | 1MSPS           | serial    | 85mW              | 75dB@50 kHz | -87dB@50kHz | TSSOP24            |
| HL7367       | 14-bit    | 1MSPS           | serial    | 85mW              | 75dB@50 kHz | -87dB@50kHz | CSOP24/TSSOP24     |

HL7367/HL7366 This chip is compatible with foreign products AD7367 pin, can be replaced, the functional structure of the chip block diagram as shown below:



### 4 Product Highlights

- ◆ Power consumption and throughput change linearly
- ◆ Wide input range/Input range configurable
- ◆ 1MSPS quantization speed